

Ho Jeong Choi

Integrated M.S.-Ph.D. Degree
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EDUCATION

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|------------------------|--|-----------------|
| Mar. 2025 ~ Present | Sungkyunkwan University Department of Semiconductor Convergence Engineering <i>Advisor: Jong-Woong Kim</i> <i>Master Student</i> | Suwon, Korea |
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RESEARCH INTERESTS

- Semiconductor package
- Semiconductor chip bonding
- Low-temperature solder development
- Stretchable displays and sensors
- MXene development